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| <u>L13</u> | L12 and ((processor or cpu) near4 (cache adj3 control\$4)) | 60 | <u>L13</u> |
| <u>L12</u> | L11 and (((processor or controller or cpu) near6 (determin\$4 or select\$4 or pick\$3 or choos\$3 or choos\$3)) near6 (access\$3 or read\$3 or writ\$4 or us\$3 or transfer\$4)) | 388 | <u>L12</u> |
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| <u>L9</u> | multiprocessor or ((second or another or multiple or plurality) adj2 (processor or cpu)) | 45110 | <u>L9</u> |
| <u>L8</u> | L5 and (((processor or controller or cpu) near6 (determin\$4 or select\$4 or pick\$3 or choos\$3 or choos\$3)) with ((block or line) near4 (allocable or free or freed or available))) | 96 | <u>L8</u> |
| <u>L7</u> | L5 and ((processor or controller or cpu) with (determin\$4 or select\$4 or pick\$3 or choos\$3 or choos\$3) with ((block or line) near4 (allocable or free or freed or available))) | 137 | <u>L7</u> |
| <u>L6</u> | L5 and ((processor or controller or cpu) with (determin\$4 or select\$4 or pick\$3 or choos\$3 or choos\$3) with (block or line)) | 1342 | <u>L6</u> |
| <u>L5</u> | L4 and l1 | 4243 | <u>L5</u> |
| <u>L4</u> | multiprocessor or ((second or another or multiple or plurality) adj2 processor) | 40533 | <u>L4</u> |
| <u>L3</u> | L2 with 11 | 28 | <u>L3</u> |
| <u>L2</u> | (cache near4 control\$4) near6 (determin\$4 or select\$4 or pick\$3 or choos\$3 or chos\$3) | 2413 | <u>L2</u> |
| <u>L1</u> | (block or line) near4 (allocable or free or freed or available) | 92239 | <u>L1</u> |